

10/506870

DT04 Rec'd PCT/PTO 03 SEP 2004

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) : An ultrasound imaging hardware and software pack ~~composed of~~ comprising:

a hardware part for ultrasound imaging preprocessing and of a software part intended to be installed on a microcomputer, the hardware part including at least one ultrasound probe ~~[[(17)]]~~ connected ~~[[(17A)]]~~ to a module ~~[[(18)]]~~ of electronic circuits at least part of which is configurable, this module comprising analog FEC circuits ~~[[(31)]]~~, an assembly of analog/digital converters ~~[[(32)]]~~, an array of logic electronic gates ~~(33, 34)~~ and a high throughput link ~~[[(35)]]~~ between this module and the microcomputer, the software part being stored on a removable medium ~~[[(26)]]~~.

2. (currently amended): The pack as claimed in claim 1, ~~characterized in that~~ wherein the array of electronic gates is software configurable into specialized chips for channel formation ~~[[(33)]]~~, into a distributed memory, into digital filters, demodulator and control interface ~~[[(34)]]~~.

3. (currently amended): The pack as claimed in claim 1 ~~or 2~~, ~~characterized in that~~ wherein the software part configures the configurable elements of the hardware part ~~(32, 33, 34)~~ and installs, on the microcomputer, programs for calculating and presenting ultrasound imaging data.

4. (currently amended): The pack as claimed in claim 3, ~~characterized in that~~ wherein the programs installed on the microcomputer comprise programs for producing ultrasound images as gray levels and/or color Doppler images, and/or for producing continuous Doppler information and/or for displaying buttons for controlling processing parameters ~~[[(13)]]~~ and/or processing for merging with external data.

5. (currently amended): The pack as claimed in ~~one of the preceding~~ claim~~[[s]]~~ 1, ~~characterized in that~~ wherein the analog/digital converter is of the software configurable sigma-delta type.

6. (currently amended): The pack as claimed in claim[[s]] 2 to 5, ~~characterized in that~~ wherein the array of electronic gates comprises circuits of Field Programmable Gate Arrays (FPGA) type.

7. (currently amended): The pack as claimed in ~~one of the preceding~~ claim[[s]] 1, ~~characterized in that~~ wherein the high throughput link transmits several digital channels.

8. (currently amended): The pack as claimed in ~~one of the preceding~~ claim[[s]] 1, ~~characterized in that~~ wherein the high throughput link is of IEEE1394 type.

9. (currently amended): The pack as claimed in ~~one of the preceding~~ claim[[s]] 1, ~~characterized in that~~ wherein it is incorporated into a medical instrumentation assembly.

10. (currently amended): The pack as claimed in claim 9, ~~characterized in that~~ wherein the instrumentation assembly comprises equipment for surgery or for microsurgery.

11. (currently amended): The pack as claimed in claim 9 or 10, ~~characterized in that~~ wherein the medical instrumentation assembly comprises apparatus providing medical data complementary to the ultrasound data.

12. (currently amended): The pack as claimed in claim 11, ~~characterized in that~~ wherein the complementary medical data comprise at least one of the following data kinds: data from optical cameras, diagnostic and gynecological and/or cardiac monitoring data.

13. (new): The pack as claimed in claim 2, wherein the software part configures the configurable elements of the hardware part and installs, on the microcomputer, programs for calculating and presenting ultrasound imaging data.

14. (new) : The pack as claimed in claim 2, wherein the analog/digital converter is of the software configurable sigma-delta type.

15. (new) : The pack as claimed in claim 3, wherein the array of electronic gates comprises circuits of Field Programmable Gate Arrays (FPGA) type.

16. (new) : The pack as claimed in claim 3, wherein the analog/digital converter is of the software configurable sigma-delta type.

17. (new) : The pack as claimed in claim 4, wherein the array of electronic gates comprises circuits of Field Programmable Gate Arrays (FPGA) type.

18. (new) : The pack as claimed in claim 5, wherein the array of electronic gates comprises circuits of Field Programmable Gate Arrays (FPGA) type.

19. (new) : The pack as claimed in claim 7, wherein the high throughput link is of IEEE1394 type.

20. (new) : The pack as claimed in claim 10, wherein the medical instrumentation assembly comprises apparatus providing medical data complementary to the ultrasound data.